

Notice of References Cited	Application/Control No. 10/004,891	Applicant(s)/Patent Under Reexamination FARABOSCHI, PAOLO	
	Examiner J. Derek Rutten	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0023959 A1	01-2003	Park, Joseph C.H.	717/151
	B	US-6,516,463 B2	02-2003	Babaian et al.	717/156
	C	US-6,367,076 B1	04-2002	Imai et al.	717/152
	D	US-5,999,738 A	12-1999	Schlansker et al.	717/156
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach", 1995, Morgan Kaufmann Publishers, ISBN 1-55860-329-8, Chapter 4
	V	Scott A. Mahlke et. al, "A comparison of full and partial predicated execution support for ILP processors", May 1995, ACM SIGARCH Computer Architecture News, ISCA '95, Volume 23 Issue 2, pp. 138-149
	W	Aho, Sethi, and Ullman, "Compilers: Principles, Techniques, and Tools", 1986, Addison-Wesley, ISBN 0-201-10088-6, Chapter 1
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.